## Wafer bonded 1.55 $\mu$ m vertical-cavity lasers with continuous-wave operation up to 105°C

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We report 105 °C continuous-wave, electrically pumped operation of a 1526 nm vertical-cavity surface-emitting laser. An InP/InGaAsP active region was wafer bonded to GaAs/AlGaAs mirrors, with a superlattice barrier to reduce the number of nonradiative recombination centers in the bonded active region. © 2001 American Institute of Physics. [DOI: 10.1063/1.1368377]

Vertical cavity surface emitting lasers (VCSELs) emitting in the 1.55  $\mu$ m wavelength region are attractive sources for optical networks. Advantages of VCSELs include low power consumption, efficient fiber coupling, and high volume, low cost manufacturing. Transmission at 1.55 µm allows for long distance transmission over optical fiber with low attenuation and dispersion. The main limitation to realizing commercially viable devices at 1.55  $\mu$ m has been poor performance at high temperature. Maximum operating temperatures of 70-85 °C are specified for sources in fiber optic networks. The commonly used InP/InGaAsP material system suffers from low characteristic temperatures and poor thermal conductivities that have slowed the rate of long wavelength VCSEL progress compared to that of GaAs/AlGaAs based short wavelength devices. A number of novel approaches have been applied to the development of 1.55  $\mu$ m VCSELs. High performance devices have been fabricated using buried tunnel junctions,<sup>1</sup> antimonide mirrors,<sup>2</sup> and metamorphic growth.<sup>3</sup> The best high temperature results to date have been achieved using wafer bonded GaAs/AlGaAs distributed Bragg reflectors (DBRs) in both electrically pumped<sup>4</sup> and integrated optically pumped<sup>5</sup> designs. The high

thermal conductivity and index contrast of the GaAs based DBRs make them an attractive choice for fabricating long wavelength VCSEL mirrors.

Top-emitting vertical cavity lasers with two wafer bonded GaAs/AlGaAs DBRs were fabricated. The device structure is shown in Fig. 1. The top mirror is a 25.5 period p-type, parabolically graded GaAs/Al<sub>0.9</sub>Ga<sub>0.1</sub>As DBR, with an oxide aperture for mode and current confinement. The p-i-n InP/InGaAsP active region contains 6 strained quantum wells. A four period InP/InGaAsP superlattice was grown on the InP side of the InP/GaAs junction in order to reduce the number of nonradiative recombination centers in the active region.<sup>6</sup> Each layer of the superlattice was 7.5 nm thick and doped p-type. The 31 period GaAs/AlAs bottom mirror is undoped. The p-contact is on top of the p-DBR and the n-contact is on the n-cladding of the active region. The lateral oxidation depth determines the device size. Devices were tested in a *p*-side up configuration without any special heat sinking.

Room temperature voltage and continuous-wave (cw) output power characteristics are shown in Fig. 2. The minimum threshold current measured was 0.9 mA for a 6  $\mu$ m device. The highest output power obtained at 20 °C was 0.65 mW. The room temperature cavity mode was at 1526 nm, and the gain peak was at 1542 nm. Both the lasing wave-



FIG. 1. Wafer bonded, top-emitting 1.55 µm VCSEL structure.



FIG. 2. Room temperature (20 °C) voltage and continuous-wave output power.

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FIG. 3. Continuous-wave light-current characteristics for ambient temperatures of 20-105 °C.

length and peak gain wavelength increase with temperature. The lasing wavelength shifts at a rate of 0.1 nm/ °C, while the peak gain wavelength increases at a rate of 0.5 nm/ °C.<sup>7</sup> Despite the unfavorable mode-gain offset for these devices, continuous wave operation was achieved at temperatures as high as 105 °C. A family of L–I curves for cw operation from 20-105 °C are shown in Fig. 3.

The improved high temperature operation compared to the previously fabricated devices<sup>4</sup> is due to a reduced diode turn-on voltage and lowered differential resistance. A simple thermal model is illustrated in Fig. 4. It is assumed that the device stops lasing once the active region reaches a certain maximum temperature,  $T_{max}$ . This maximum active region temperature is constant and independent of the ambient temperature.  $T_{\text{max}}$  is equal to the ambient temperature,  $T_{\text{amb}}$ , plus the thermal resistance,  $R_{\rm th}$ , multiplied by the dissipated electrical power,  $P_{dis}$ , at rollover. This can be expressed as the equation of a straight line, given by  $T_{\text{max}} = T_{\text{amb}}$  $+P_{\rm dis}R_{\rm th}$ . The data points indicated by squares show the electrical power dissipated at the bias point where the L-I characteristic has rolled over and the device is no longer lasing. The data points indicated by triangles show the electrical power dissipated at threshold by the device with the L-I characteristics shown in Fig. 3. The continuous-wave operating regime, shaded, is bound by the threshold and rollover dissipated power curves. A thermal resistance of 0.41 °C/mW is extracted from the linear fit for a device defined by a 40  $\mu$ m etched pillar and 6  $\mu$ m oxide aperture. The maximum active region temperature that will support lasing is determined to be 120 °C. The simple fit used to extract these parameters treats the active region temperature as a one-dimensional distribution. A higher degree of accuracy could be obtained by using a two- or three-dimensional model to determine local heating within the active region. The improved electrical properties reduce the dissipated power and device self-heating. In addition, the superlattice



FIG. 4. Thermal resistance and maximum operating temperature.

barrier preserves active region luminescence during the bonding process and reduces the number of nonradiative recombination centers. Improved high temperature performance, including higher output powers over the operating temperature range, should be possible with improved modegain offset and reduced differential resistance. The high turn-on voltage and resistance are due in part to the electrical characteristics of the *p*-InP/*p*-GaAs bonded junction.<sup>8</sup> Optimization of the doping and bonding conditions should result in lower voltage operation.

Electrically pumped, wafer bonded VCSELs with a lasing wavelength of 1526 nm were fabricated. The maximum cw operating temperature achieved was 105 °C. The maximum output power at 20 °C was 0.65 mW. The maximum output power at 80 °C was 0.22 mW. Threshold currents of 0.9 mA were obtained for a 6  $\mu$ m device. Significant improvements are expected for devices with a more favorable mode-gain offset, lower turn-on voltage, and reduced series resistance.

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