Heterogeneous Integration for Mid-Infrared Silicon Photonics

Alexander Spott, Student Member, IEEE, Eric J. Stanton, Student Member, IEEE, Nicolas Volet, Jon D. Peters, Jerry R. Meyer, Fellow, IEEE, and John E. Bowers, Fellow, IEEE

Abstract—Heterogeneous integration enables the construction of silicon (Si) photonic systems, which are fully-integrated with a range of passive and active elements including lasers and detectors. Numerous advancements in recent years have shown that heterogeneous Si platforms can be extended beyond near-infrared telecommunication wavelengths to the mid-infrared (MIR) (2–20-µm) regime. These wavelengths hold potential for an extensive range of sensing applications and the necessary components for fully-integrated heterogeneous MIR Si photonic technologies have now been demonstrated. However, due to the broad wavelength range and the diverse assortment of MIR technologies, the optimal platform for each specific application is unclear. Here we overview Si photonic waveguide platforms and lasers at the MIR, including quantum cascade lasers (QCLs) on Si. We also discuss progress towards building an integrated multi-spectral source, which can be constructed by wavelength beam combining the outputs from multiple lasers with arrayed waveguide gratings (AWGs) and duplexing adiabatic couplers.

Index Terms—Silicon Photonics, Semiconductor Lasers, Quantum Cascade Lasers, Photonic Integrated Circuits

I. INTRODUCTION

T he vast majority of research on silicon (Si) photonics has focused on devices operating within the near-infrared (NIR) telecommunication bands near 1310 nm and 1550 nm wavelengths. This has been driven chiefly by the prospect of low-cost photonics fabricated with existing complementary metal-oxide-semiconductor (CMOS) infrastructure and the large market for telecommunications with ever increasing demands. While alternative applications of Si photonics and the benefits of integrating optical devices which function at wavelengths outside the telecommunications bands have not been overlooked, additional challenges have faced the development of longer-wavelength devices.

The mid-infrared (MIR) spectrum, typically defined from 2 µm to 20 µm, demands great interest primarily for gas sensing, defense, and medical applications. Strong and wavelength-selective interactions of light in this regime with many chemicals [1] can be exploited to distinguish substances with multiple spectroscopic techniques. Standoff detection systems, which operate within the MIR transparency windows, can allow remote identification of hazardous materials.

Technologies that address these applications can be miniaturized with chip-scale photonic integration. Si promises a platform for integration solutions at minimal cost due to the existing infrastructure, low material costs, and inherent compatibility with integrated Si electronics systems. It also offers physical advantages compared to other nanophotonic integration material platforms, such as indium phosphide (InP), gallium arsenide (GaAs), or chalcogenide glasses [2]. A high index contrast between Si and deposition-compatible dielectric materials such as silicon nitride (SiN) or silicon dioxide (SiO2) allows light to be well confined within the Si core, enabling exceptionally small devices. The low optical absorption of Si permits extremely high-Q cavities [3], enhancing the sensitivity of integrated sensors. As discussed in this manuscript, it is possible to construct an extremely broadband platform on a Si substrate, which maintains these advantages for waveguides operating from the NIR to the MIR.

The primary challenge for realizing a fully integrated Si photonic platform is to make an efficient light source. Si has an indirect bandgap and corresponding low emission efficiency, which has limited the progress of using Si to generate light. By wafer bonding III-V materials onto processed Si waveguides, heterogeneously integrated III-V/Si devices can be manufactured [4]. This approach allows III-V devices to be constructed on Si with lithographically-limited alignment to Si waveguides. Epitaxial (epi) layer stacks from different III-V growths can be bonded to different areas of a Si chip, allowing a single system to include the III-V materials best suited for each device. This multiple die bonding has been used, for example, to build an integrated triplexer which combines three III-V lasers (operating at 1.31 µm, 1.49 µm, and 1.55 µm) [5]. The development of Si photonic and heterogeneous integration technologies has progressed rapidly in recent years and photonic integrated circuits (PICs) have now been demonstrated with up to 426 passive and active elements for the NIR [6]. Few PICs have been constructed for operation in the MIR [7,8], however, although numerous individual passive and
active devices for the MIR have been successfully integrated on Si by extending NIR device designs to longer wavelengths. Heterogeneous integration has enabled detectors up to 3.8 \( \mu \text{m} \) [7,9] and, as overviewed here, lasers up to 4.8 \( \mu \text{m} \). All the necessary components for fully integrated MIR Si photonic systems have now been demonstrated.

![Fig. 1. Scanning electron microscope (SEM) cross sections of (a) SOI and (b) SONOI waveguides with 1.5-\( \mu \text{m} \) thick Si waveguides for MIR operation.](image)

II. ARCHITECTURE AND MATERIAL SYSTEMS

Si, SiO\(_2\), and Si\(_3\)N\(_4\) have low optical material losses in the NIR, allowing Si waveguides with propagation losses below 1 dB/cm [10] and Si\(_3\)N\(_4\) waveguides with ultra-low losses below 0.1 dB/m [11]. This, along with the high index contrast afforded by Si-on-insulator (SOI) waveguides, is extremely convenient given the wide availability and high quality of SOI wafers.

However, the material absorption of Si and SiO\(_2\) limits the wavelength range of SOI waveguides. On the shorter-wavelength side of its transparency, the Si absorption begins increasing at \( \approx 1.2 \mu \text{m} \). While at longer wavelengths Si continues to transmit light up to \( \approx 6.8 \mu \text{m} \), absorption in the SiO\(_2\) begins to increase dramatically for wavelengths longer than \( \approx 3.5 \mu \text{m} \). Ultimately, the practical wavelength compatibility of SOI waveguides depends on the required transmission losses and the waveguide geometry, which dictate the proportion of light guided within each material.

For relatively large waveguide dimensions, it is still possible to fabricated MIR SOI waveguides with propagation losses smaller than \( \approx 5 \) dB/cm. A cross-section scanning electron micrograph (SEM) of a 1.5-\( \mu \text{m} \) tall SOI waveguide is shown in Fig. 1(a). Fig. 2 shows preliminary cutback measurements of 2-\( \mu \text{m} \) wide waveguides measured at \( \lambda \approx 4.8 \mu \text{m} \). The experimental setup is schematized in Fig. 2(a). An interband cascade laser (ICL) emits MIR light, which is collimated by a Si lens. This free-space beam passes through a quarter-wave plate (QWP) and a half-wave plate (HWP), which are rotated to align the on-chip polarization to either transverse electric (TE) or transverse magnetic (TM). A parabolic mirror (Thorlabs RC02APC) couples the light into a fiber (Thorlabs P3-32F), which is aligned to the input facet with a 3-axis flexure stage (Thorlabs MAX312D) and similarly collected at the output facet. Transmitted power is measured with a photodetector (Boston Electronics PVI-4TE-5) aligned to the output of the collection fiber.

The transmission of waveguides both with and without a plasma-enhanced chemical vapor deposition (PECVD) SiO\(_2\) upper cladding are used to extract the loss. Fig. 2(b) shows the measurements for TE-polarized light, which yield losses of 3.54±0.11 dB/cm with cladding and 3.00±0.69 dB/cm without cladding. Fig. 2(c) shows the corresponding measurements for TM polarized light, for which the losses are 3.51±0.51 dB/cm.

Alternative material systems can be considered to construct a Si photonics platform for longer wavelengths. A number of waveguide schemes have previously been suggested [13] and demonstrated, including Si-on-sapphire (SOS) [14], germanium-on-Si (GOS) [15,16,17], germanium-on-Si\(_3\)N\(_4\) (GON) [18], Si-on-Si\(_3\)N\(_4\) (SON) [19], suspended Si [20], and chalcogenide-on-Si [21,22]. These platforms are each suitable for different wavelength ranges and offer other advantages and disadvantages. The ultimate waveguide propagation loss limits are still being explored, with most reported loss values between 1–5 dB/cm. Because sidewall and surface roughness loss are proportional to \( 1/\lambda^2 \), losses below 1 dB/cm should be easily achievable in MIR waveguides if material absorption is not the limiting factor.

Among these platforms, one of the more straightforward waveguide schemes is to replace the lower SiO\(_2\) cladding of SOI waveguides with Si\(_3\)N\(_4\) and construct SON waveguides. Si\(_3\)N\(_4\) is easily deposited by PECVD and low pressure chemical vapor deposition (LPCVD) processes, which are CMOS compatible and commonly used in existing Si photonic systems. These waveguides should maintain low propagation losses at wavelengths from \( \approx 1.2 \mu \text{m} \) to \( \approx 6.7 \mu \text{m} \).

If a broader wavelength range is desired, a platform can be constructed with multiple waveguiding layers [23], which...
operate within different spectral limits. For shorter wavelengths where Si absorption is limiting, Si$_3$N$_4$-on-SiO$_2$ waveguides have previously been used [11] and should be suitable for wavelengths from 350 nm to 3.5 µm. The Si-on-nitride-on-insulator (SONOI) platform, as shown in Fig. 1(b), can be constructed by including an additional SiO$_2$ layer underneath the Si$_3$N$_4$ layer of a SON waveguide. This allows the use of both Si-on-Si$_3$N$_4$ waveguides for longer wavelengths and, by etching the top Si layer, Si$_3$N$_4$-on-SiO$_2$ waveguides for shorter wavelengths.

III. INTEGRATED LIGHT SOURCES

Active elements can be heterogeneously integrated using any of these material systems by bonding III-V materials above the passive device layer after waveguide processing. Light sources have been integrated with Si waveguides for wavelengths from 1.3 µm [24] to 4.8 µm and detectors for wavelengths up to 3.8 µm. Active regions not in a cavity can also be integrated to provide semiconductor optical amplifiers (SOAs) above Si waveguides.

The results and designs discussed here are achieved with low temperature hydrophilic bonding of InP to Si, where an oxygen plasma activation is applied to the surfaces of both materials followed by a 300 °C anneal for about 1 h [25]. Other results use a thin DVS-BCB interlayer between materials. BCB absorption is low at wavelengths up to 3-µm [26], but may increase the internal device loss for longer wavelengths.

Fig. 3 shows a generalized schematic of a heterogeneously integrated laser source. Here, a III-V mesa sits above a Si waveguide to form a hybrid III-V/Si active region. The III-V mesa is tapered on both sides to transfer light from the hybrid active region to passive Si waveguides on both ends. Tapering schemes can be designed with simultaneous tapering of the Si waveguide to optimize the mode coupling efficiency. The most common design, shown in Fig. 4(a), includes a very wide (typically near 24 µm) III-V mesa which sits above a narrow Si waveguide. The optical mode is laterally confined primarily by the width of the Si waveguide, while proton implantation is implemented to form high resistivity regions which confine carriers within a narrow central current channel above the Si waveguide. A careful selection of the Si waveguide and current channel widths is required to ensure single mode operation and optimally overlap the fundamental optical mode with the injected carrier distribution in order to maximize gain and minimize losses. The potential for lasing in parasitic higher-order modes is mitigated as these modes tend to spread laterally, reducing the interaction with the active region in the current channel.

In Fig. 4(b), a narrow III-V mesa laterally confines both the current channel and the optical mode. This design more closely resembles a traditional narrow ridge laser on a native substrate, and is more suitable for lasers with upper cladding materials which are incompatible with proton implantation. A sufficiently narrow III-V mesa is required to completely cut off higher order modes, which can be difficult to achieve at shorter wavelengths. It also increases the interaction of the desired fundamental mode with the mesa sidewalls, potentially adding sidewall roughness-induced internal loss. It is possible to achieve a higher optical confinement factor with this design compared to lasers with wide III-V mesas.

Finally, the design shown in Fig. 4(c) combines a narrow upper cladding with a wide active region. In this design a current channel is defined without proton implantation, but higher order modes still spread laterally as with the proton implanted designs. The active region and SCH layers must therefore be sufficiently thick or the narrow upper cladding should only be partially etched. This scheme has primarily been employed for integrated quantum cascade lasers (QCLs) reviewed here, with a 1510-nm thick active core.

Heterogeneously integrated lasers with an interlayer such as BCB or SiO$_2$ have also be constructed with no Si waveguide within the active region.

A recent in-depth review of NIR heterogeneously integrated lasers and amplifiers can be found in [27].
B. 2.0-µm Lasers and Amplifiers on Si

A variety of type-I and type-II diode lasers emit in the 2–3-µm range, with varying performance for each technology. Integrating an InP-based type-I laser with InGaAs quantum wells is the most straightforward progression from heterogeneously integrating NIR lasers, and was the focus of the initial efforts to fabricate MIR lasers on Si. We used the SOI platform for this work, which is still suitable for wavelengths near 2.0 µm.

The successful integration of a room-temperature continuous-wave (CW) laser with Si waveguides was only recently demonstrated in this wavelength range [40]. The III-V material used for this work (grown at nLight, Inc.) contained 4 InGaAs quantum wells for operation at 2.0-µm wavelength. The lasers were integrated above 500-nm thick, partially etched Si waveguides.

Lasers were fabricated with designs similar to those shown in Fig. 4(a) and 4(b) above. The devices with a 24-µm wide active region design included 4–6-µm wide current channels formed by proton implantation. Fig. 6(a) shows a schematic of the active region of these devices. Unimplanted lasers with narrow 4–6-µm wide mesa were also fabricated.

Due to the longer wavelength, the III-V material stack of these devices was slightly thicker than is typical for NIR lasers. The 2.4-µm thick upper cladding was etched with a methane-hydrogen-argon (MHA) reactive ion etch (RIE) which was stopped with a laser monitor near the bottom of the InP layer. The field etch depth was then planarized with a selective HCl:H3PO4 (1:3) wet etch which stopped on the InP bottom contact layer. Since MIR laser gain material composition depends significantly on the wavelength and active-region technology, precise fabrication steps and uniform control of the mesa etch depths must be optimized separately for each integration technique.

The following sections present an overview of MIR heterogeneously integrated lasers and SOAs on Si.

A. MIR Lasers on Si

The flexibility of heterogeneous integration provides the choice of which III-V gain material option is best for each wavelength and application. This is particularly important for MIR wavelengths as no single semiconductor laser technology operates throughout the entire broad 2–20-µm regime, and the performance of each varies with wavelength. Table 1 shows nominal wavelength ranges for the most prominent semiconductor laser technologies which are capable of emitting light at room temperature for $\lambda > 2 \mu m$.

For the 2–3-µm wavelength range, InP- and GaSb-based diode lasers [28] perform well. For wavelengths above 3 µm, commercially available GaSb-based interband cascade lasers (ICLs) produce hundreds of mW [29,30] with high efficiency, and InP-based quantum cascade lasers (QCLs) generate Watts [31] and function out to 20 µm [32] and in the terahertz regime [33].

<table>
<thead>
<tr>
<th>Substrate</th>
<th>Technology</th>
<th>Wavelength Range (µm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>InP</td>
<td>Type-I diode</td>
<td>NIR-2.4 [54]</td>
</tr>
<tr>
<td>InP</td>
<td>Type-II diode</td>
<td>2.0–2.6 [35]</td>
</tr>
<tr>
<td>GaSb</td>
<td>Type-I diode</td>
<td>2.9–3.6 [36,37]</td>
</tr>
<tr>
<td>GaSb</td>
<td>Interband cascade laser</td>
<td>2.9–7.0 [38]</td>
</tr>
<tr>
<td>InP, GaAs, InAs</td>
<td>Quantum cascade laser</td>
<td>3.0–20 [39]</td>
</tr>
</tbody>
</table>

TABLE I

MIR SEMICONDUCTOR LASER TECHNOLOGIES

Fig. 5 shows a generalized process flow for an integrated III-V gain material above Si waveguides. Active layers grown on InP, GaAs, and gallium antimonide (GaSb) substrates have been successfully bonded to Si with high yield, although the annealing and substrate removal processes differ in each case.

![Fig. 5. Generalized process flow of a heterogeneously integrated III-V element with a Si waveguide. (a) Si processing including waveguide and grating etching. (b) Bond III-V layers and remove substrate. (c) Etch III-V mesa, stopping within bottom contact layers. (d) Deposit metal for bottom and top electrodes. (e) Optional: proton implantation to define a current channel. (f) Etch vias and deposit probe metal.](image-url)
Fabry-Perot [41] and distributed feedback [42] lasers operating at wavelengths near 2.3 \( \mu m \), GaSb-based lasers showing pulsed operation near 2.38 \( \mu m \) from preliminary measurements [26], and an InP-based laser with InGaAs quantum wells operating in pulsed mode near 1.9 \( \mu m \) [43].

The same gain structures can also be used in an SOA configuration [44], by anti-reflective coating the Si waveguide end-facets. Light from a tunable laser emitting near 2.0–2.06 \( \mu m \) was injected with a lensed fiber to test the gain properties of these devices. The SOA emission was collected with another lensed fiber from the opposite Si facet, and monitored with an optical spectrum analyzer.

For 2.01-\( \mu \)m light, on-chip gain with a peak value of over 13-dB and 3-dB bandwidth of ~75 nm was observed at 20 °C. These SOAs operated up to 50 °C. Fig. 7(a) shows the on-chip gain as a function of drive current for an SOA pumped with wavelengths in the range 2.01–2.06 \( \mu m \), while Fig. 7(b) shows the on-chip gain as a function of wavelength for various drive currents.

The first quantum cascade lasers were integrated on Si in 2016 and operated at a wavelength near 4.8 \( \mu m \) [45]. Like the \( \lambda = 2.0 \mu m \) InGaAs MQW lasers, these first-generation devices were designed to form Fabry-Perot laser cavities defined by reflections from polished Si facets. To establish a broadband and versatile heterogeneous platform, the QCLs were integrated with the SONOI platform. Fully etched Si waveguides with height 1.5 \( \mu m \) were constructed on top of a 400-nm thick Si$_3$N$_4$ layer and 3-\( \mu m \) thick SiO$_2$ layer on a Si substrate.

Fig. 8(a) shows a cross-sectional schematic of the active region of a heterogeneously integrated QCL. Unlike conventional heterogeneously integrated NIR lasers or the integrated 2.0-\( \mu m \) wavelength lasers discussed above, proton implantation was not used for the QCLs to define a current channel. Most devices had an etched mesa design as in Fig. 4(b), while some were fabricated with a wide active region design as in Fig. 4(c). The optical confinement in the active region ranged from \( \Gamma \approx 0.73 \) to 0.76 depending on the fabrication process.
device geometry. The hybrid III-V/Si active region was 4-mm long and the III-V tapers were either 20 μm or 45 μm long.

These lasers were grown on an InP substrate by metalorganic chemical vapor deposition (MOCVD) at the University of Wisconsin. The active core layers included 30 stages similar to those shown in [46]. The QCL epilayer stack designed for heterogeneous integration differed significantly from typical MQW designs. In particular, the active/injector stages composed a 1510-nm-thick region and the upper n-InP cladding and contact layers were thickened to 4050 nm to prevent optical overlap with the metal on top of the mesa. Consequently, the total QCL epi thickness of over 5.5 μm is more than double the typical thickness of ~2 μm for an integrated NIR diode laser [27]. To maintain the high active-region optical confinement needed for a QCL, the lower n-InP contact layer was 200-nm thick.

As a result, for heterogeneously integrated QCLs it is challenging to maintain the high etch precision required to define a tall mesa while stopping within a thin contact region, uniformly across the chip. To achieve this, an MHA RIE was used to etch through the upper n-InP layers and the active region was etched with H3PO4:H2O2:H2O (1:5:15), which selectively stopped at the n-InP cladding below. Unfortunately, this wet etch through ~1.5 μm resulted in a significant undercut of the active region, which reduced the active region taper tip length by as much as several microns on some devices. This is likely the cause of the reduced taper performance observed and discussed below.

These Fabry-Perot lasers operated in pulsed mode (250-ns wide pulses at 1-kHz repetition rate) at room temperature [44]. Minimum threshold currents of 387 mA (1.6 kA/cm²) were measured, and up to 31 mW of single-sided output power was collected from a passive SONOI waveguide. Fig. 8(b) shows the light emission vs. drive current of a QCL with a 6-μm wide active region and 1.5-μm wide Si waveguide. Although pulsed, this is comparable to the highest output powers reported for heterogeneously integrated lasers of any wavelength [27]. The threshold current density of 1.6 kA/cm² is reasonably low compared to QCLs grown on native InP substrates, indicating a low internal loss within the cavity. However, the differential efficiencies in the range 150–170 mW/A are much lower than what is expected from QCLs with similar active region compositions and threshold current densities. Further analysis indicated that low taper transition efficiency and high taper reflections are most likely the cause of this performance reduction.

Fig. 8(c) shows the light emission vs. drive current for an integrated QCL, measured at temperatures in the range 10–60 °C. The corresponding characteristic temperatures of $T_0 = 175$ K and $T_1 = 87$ K for threshold and slope efficiency, respectively, are comparable to those of QCLs grown on native InP substrates [46,47].

While internal reflections that depend on polished waveguide facets are not suitable for integration within a photonic integrated circuit, wavelength-selective feedback elements, which we have integrated with lasers in the NIR [48], can be applied at longer wavelengths. In fact, Bragg reflectors for the mid-infrared are easier to define with photolithography as the grating pitch required for a first-order grating scales with wavelength.

Because the Si waveguides are processed before wafer bonding, heterogeneous integration allows the inclusion of a grating on top of Si the waveguides within the active region [49]. We consequently demonstrated distributed feedback (DFB) QCLs on Si [50] by incorporating a λ/4-shifted first-order grating throughout a 3-mm long active region, as illustrated in Fig. 9(a).
The DFB lasers measured lower peak single-sided emission of ~11 mW, output powers than the previous Fabry-Perot lasers, with a characterization repeated by measuring output from the uniform carrier distribution in the active region. (b) Pulsed light emission from a 2-mm long DFB QCL with 4-μm wide narrow mesa. µm wide upper n-InP cladding above a 24-μm wide QCL active region.

The threshold current densities of the DFB QCLs were typically below 1 kA/cm². Like the Fabry-Perot devices, these lasers also had a poor taper transmission efficiency, and initial characterization of the DFB lasers measured lower peak output powers than the previous Fabry-Perot lasers, with a maximum single-sided emission of ~11 mW.

Polished hybrid III-V/Si facet, an example of which is shown in Fig. 9(b). This configuration yielded threshold current densities below 1 kA/cm², and a minimum threshold current of 131 mA. Fig. 10(a) shows the light output vs. current density for a device with a wide active region design (Fig. 4(c)). Up to 211 mW of peak power was generated after the III-V taper was removed, and the maximum slope efficiency increased to 541 mW/A.

It is also possible to build smaller DFB lasers with lower threshold drive currents, e.g., recently-tested lasers with 2-mm long active regions displayed similar threshold current densities of near 1 kA/cm². Fig. 10(b) shows the light emission characteristics of a device with a threshold current of 80-μA.

Fig. 10(c) and 10(d) show the optical spectrum and light output vs. drive current, respectively, for an integrated DFB QCL operating at temperatures from 10 °C to 100 °C. Wavelength selectivity corresponding to the Bragg frequency of the DFB grating was observed and 22 nm of thermal tuning was achieved. However, the majority of devices did not exhibit true single-wavelength operation, most likely due to spurious reflections from the taper tip. Extracted characteristic temperatures were $T_0 = 199$ K and $T_1 = 222$ K, which are significant improvements compared to the Fabry-Perot lasers.

Although reflections and loss from the III-V tapers limited the performance of these lasers, in future processing runs it should be possible to significantly improve the efficiency of the taper transitions with improved design and fabrication. Furthermore, the thermal characteristics and relatively low threshold current densities of these lasers imply that CW lasing should be possible with improved heat dissipation. Possible solutions include the introduction of thermal shunts [51], or by bonding a thermally conductive submount to the top of the chip.

QCL gain material has been used for amplification in the past [52] and the same can be achieved on silicon to construct integrated SOAs as was achieved at 2.0 μm. The active stages of a QCL can also be designed for operation at other wavelengths without significantly modifying the surrounding cladding layers. It is therefore possible to apply these techniques at longer or shorter wavelengths to provide laser sources throughout the MIR transmission window of Si waveguides, up to wavelengths near 7 μm.

D. >7-μm Lasers on Si

For wavelengths beyond 7 μm, the SOI and SONOI platforms are unlikely to provide hybrid waveguides with sufficiently low loss to support lasing. However, integrated QCLs should still be practical using an alternative platform such as one based on germanium (Ge) or germanium-silicon (GeSi) waveguides. Fig. 11(a) schematically illustrates two similar options for placing a Ge waveguide above a Si layer or substrate. Ge-on-Si (GOS) or Ge-on-SOI (GOSOI) waveguides can guide light from 1.9 μm to above 10 μm. As with the SONOI platform, removing the Ge layer from the Ge-on-SOI platform will allow the fabrication of SOI waveguides that permit transmission at shorter wavelengths throughout the NIR, if the layer thicknesses are properly designed.
As with SOI and SONOI heterogeneous integration, low temperature, plasma-assisted, hydrophilic wafer bonding can be applied to bond QCL layers above GOS or GOSOI waveguides. Fig. 11(b) shows a cross-sectional schematic of a hybrid GOS/III-V active region analogous to the QCL configuration shown in Fig. 8(a). Overlaid is a contour plot of the electric field component \( |E_y| \) of the fundamental TM optical mode, which has a confinement with the active region of \( \Gamma \approx 0.77 \). Due to the high refractive index of Ge, and therefore the high effective index of GOS waveguides, a slightly narrower Ge waveguide is necessary to push the optical mode higher into the QCL mesa. This high refractive index may allow more efficient III-V taper transitions.

IV. INTEGRATED SPECTRAL BEAM COMBINING

Because heterogeneous integration allows multiple lasers operating at different wavelengths to be integrated on one chip, spectral beam combining of any of the lasers discussed in this manuscript can be used to construct a multispectral light source [23]. For densely spaced spectral channels, arrayed waveguide gratings (AWGs) have been demonstrated with low loss spanning the visible (VIS) [53] to NIR [54], and moderate loss in the MIR [55,56].

Two methods can be used to align each laser emission wavelength to the AWG spectral combiner channel. First, the lasers can be tuned to align with the combiner channel with a feedback that maximizes the output power. The AWG can also be tuned, although that approach may be more difficult and add more loss to the AWG transmission. The second option is to design the AWG within the laser cavity. This is typically avoided since it would compromise the performance of the lasers and result in lower output power and brightness. However, with ultra-low loss AWGs having < 0.5 dB of loss per channel, this may nonetheless be advantageous when applied to a multi-spectral laser with many channels.

Fig. 12 shows the schematic for a multispectral source integrated on a single SONOI platform that combines the beams from bonded III-V lasers emitting at wavelengths ranging from 0.4 µm to 7 µm. In a first (intra-band) combination stage, the light produced by arrays for each band is combined into a single output waveguide by an AWG. In the second (inter-band) combination stage, light from multiple spectral bands is combined with an AWG designed to have a much coarser wavelength spacing. On a SONOI waveguide platform, the light at shorter wavelengths is generated in Si\(_3\)N\(_4\) waveguides while the light at longer wavelengths (including the MIR) is generated in Si waveguides. Then in the final combination stage, an ultra-broadband duplexer is used to combine all channels [57].

Fig. 12. Multi-spectral laser architecture with multiple gain materials bonded onto a Si substrate. Each laser output is spectrally combined in multiple stages to a single output waveguide.
This technique is advantageous only if the loss in the spectral combiners is reduced to less than about 3 dB per channel [58]. SOI-based AWGs should provide such low loss for wavelengths up to ~3.6 µm, after which SiO₂ absorption becomes detrimental. Fig. 13(a) shows a micrograph of an AWG fabricated on SOI and designed to have a center wavelength of ~3.61 µm and channel spacing of ~10 nm. The simulated transmission spectra shown in Fig. 13(b) demonstrates low loss in the range 0.2–1.0 dB, and a crosstalk of ~45 dB per channel.

V. CONCLUSION

We have overviewed Si photonic platforms and the heterogeneous integration of III-V materials with Si waveguides in the context of MIR operation. Recent lasers integrated on Si have operated at wavelengths near 2.0 µm and 4.8 µm.

InP-based type-I laser diodes integrated with SOI waveguides emit more than 9 mW of CW power at λ = 2.01 µm at room temperature. Integrated SOAs with the same active material provide over 13 dB of peak gain at λ = 2.01 µm.

QCLs for λ = 4.8 µm operation were integrated on the broadband SONOI waveguide platform. Fabry-Perot lasers emitted 31 mW into silicon waveguides and operated in pulsed mode up to 60 ºC. DFB lasers with threshold currents as low as 80 mA and threshold current densities below 1 kA/cm² emitted > 200 mW from a polished III-V/Si facet, and operated in pulsed mode up to 100 ºC. Although design and fabrication modifications are necessary to improve the III-V taper transmission and to achieve CW operation, these lasers are suitable for integration into photonic integrated circuits.

By bonding QCL materials designed for emission at other wavelengths above SONOI waveguides, it should already be possible to build a mid-infrared system on silicon that combines sources and devices operating at wavelengths from 3 to 6.7 µm.

REFERENCES


Alexander Spott received his B.S. degree in Physics from University of Washington, Seattle, in 2011 and his M.S. degree in Electrical and Computer Engineering from University of California, Santa Barbara, in 2015. He is currently pursuing a Ph.D. degree at the University of California, Santa Barbara.

His research has focused on mid-infrared optoelectronics and silicon photonics. He received a National Science Foundation (NSF) Graduate Research Fellowship in 2012.

Eric J. Stanton received his B.S. degree in Electrical Engineering from Cal Poly San Luis Obispo in 2012 and M.S. degree in Electrical and Computer Engineering from the University of California Santa Barbara (UCSB) in 2014. He is currently pursuing his Ph.D. at UCSB with emphasis in photonics, focusing on devices for multi-octave spectral combining of heterogeneously integrated lasers on silicon.

Nicolas Volet received his Ph.D. degree in Physics from École polytechnique fédérale de Lausanne (EPFL), Switzerland, in 2014. He developed a new technology to control the optical mode of vertical-cavity surface-emitting lasers (VCSELs) operating in the O- and in the C-bands. He also contributed to the understanding of the polarization properties of these devices, both theoretically and experimentally.

In 2006-2007, he was awarded a one-year scholarship at Carnegie Mellon University (CMU) in Pittsburgh and in 2008-2009, he worked on solar cells based on quantum confinement at the University of Houston. He was awarded research fellowships from the Swiss National Science Foundation (SNSF) in 2014 and in 2016. He is currently a post-doctoral researcher at the University of California, Santa Barbara (UCSB), where he works on non-linear optics and photonics devices heterogeneously integrated on silicon.

Jon Peters has over 13 years of process experience in the MEMS and semiconductor field. He worked at Applied Magnetics Corporation in Santa Barbara as an operator, process inspector, and plate and etch technician for five years before moving to Solus Micro Technologies/NP Photonics in Westlake Village as a Process Engineer for four years. Jon moved back to Santa Barbara to work at Innovative Micro Technology as a Process Engineer and R & D Engineer for four years before coming to work for the Bowers group at UCSB in 2008.

Jerry R. Meyer completed his Ph.D. in Physics at Brown University in 1977. Since then he has carried out basic and applied research at the Naval Research Laboratory in Washington DC, where he is the Navy Senior Scientist for Quantum Electronics (ST). His research has focused on semiconductor optoelectronic materials and devices such as new classes of semiconductor lasers and detectors for the infrared. He is a fellow of OSA, APS, IEEE, IOP, SPIE. He is a recipient of the Presidential Rank Award, ONR’s Captain Robert Dexter Conrad Award for Scientific Achievement (2015), NRL’s E. O. Hulbert Annual Science Award (2012), and the IEEE Photonics Society Engineering Achievement Award (2012). He has co-authored over 370 refereed journal articles which have been cited more than 20,000 times (H-Index 58), 16 book chapters, 33 patents (7 licensed), and over 160 invited conference presentations.

John E. Bowers holds the Fred Kavli Chair in Nanotechnology, and is the Director of the Institute for Energy Efficiency and a Professor in the Departments of Materials and Electrical and Computer Engineering at University of California, Santa Barbara. He is a cofounder of Aurrion, Aerius Photonics and Calient Networks. Dr. Bowers received his M.S. and Ph.D. degrees from Stanford University and worked for AT&T Bell Laboratories and Honeywell before joining UC Santa Barbara. Dr. Bowers is a member of the National Academy of Engineering and the National Academy of Inventors. He is a fellow of OSA, OSA and the American Physical Society, and a recipient of the IEEE Photonics Award, OSA Tyndal Award, the OSA Holonyak Prize, the IEEE LEOS William Streifer Award and the South Coast Business and Technology Entrepreneur of the Year Award. He has published eight book chapters, 700 journal papers, 900 conference papers and has received 64 patents. He and coworkers received the EE Times Annual Creativity in Electronics (ACE) Award for Most Promising Technology for the hybrid silicon laser in 2007.