Highly linear heterogeneous-integrated Mach-Zehnder interferometer modulators on Si

CHONG ZHANG,1,* PAUL A. MORTON,2 JACOB B. KHURGIN,3 JON D. PETERS,1 AND JOHN E. BOWERS1

1Department of Electrical and Computer Engineering, University of California, Santa Barbara, CA 93106, USA
2Morton Photonics, West Friendship, MD, USA
3Department of Electrical and Computer Engineering, Johns Hopkins University, Baltimore, MD, USA
*czhang@ece.ucsb.edu

Abstract: In this paper we demonstrate highly linear Mach-Zehnder interferometer modulators utilizing heterogeneous integration on a Si substrate (HS-MZM). A record high dynamic range was achieved for silicon devices, obtained using hybrid III-V/Si phase modulation sections and single drive push-pull operation, demonstrating a spurious free dynamic range (SFDR) of 112 dB·Hz2/3 at 10 GHz, comparable to commercial Lithium Niobate MZMs.

©2016 Optical Society of America

OCIS codes: (130.4110) Modulators; (060.5625) Radio frequency photonics; (230.4205) Multiple quantum well (MQW) modulators.

References and links
1. Introduction

The non-linearity of an electro-optic modulator causes signal distortion, reducing its dynamic range, therefore limiting its application in analog radio frequency (RF) photonics systems. Commercial Lithium Niobate (LiNbO$_3$) Mach-Zehnder interferometer modulator (MZM) devices are widely used in such systems [1, 2], however, these devices are bulky and expensive, limiting addressable applications. Semiconductor based devices offer the chance to obtain highly linear performance together with small size, low cost, and the ability to integrate them with additional components to form photonic integrated circuit (PIC) devices. High performance MZMs have been developed based on GaAs [3] and InP [4], with the InP devices offering the opportunity to combine the MZM with other components (e.g. laser, photodetector) to develop photonics integrated circuits [5] for commonly used telecom bands.

The silicon photonics platform takes advantage of the tremendous investment and expertise in designing and fabricating devices on large Si wafers using standard CMOS foundry processes; enabling large scale integration with high repeatability, plus high volume manufacturing with low cost. An MZM fabricated on a silicon photonics platform is therefore of great interest [6, 7]. Silicon MZMs, while significantly smaller than LiNbO$_3$ MZMs, are still relatively long for a semiconductor modulator due to low phase modulation efficiency [8], requiring either longer MZM lengths or higher modulation voltages. Additionally, and more importantly for analog systems, the nonlinearity of a Si MZM is significantly increased compared to a LiNbO$_3$ MZM, due to the inherent nonlinearity of the Si phase modulation mechanism based on free carrier insertion or depletion. While a typical optical link using a LiNbO$_3$ MZM may have a spurious free dynamic range (SFDR) of up to 113 dB·Hz$^{2/3}$ [9], much lower values have been found with Si MZM devices. Some improvement in the linearity of Si MZMs was achieved by using differential drive to reduce the non-linearity of the Si PN junction phase section, providing an SFDR of up to 97 dB·Hz$^{2/3}$ at 1 GHz modulation frequency [10]. Record high SFDR of 106 dB·Hz$^{2/3}$ at 1 GHz, and 99 dB·Hz$^{2/3}$ at 10 GHz was obtained for Si based modulators using a Ring Assisted Mach-Zehnder Interferometer (RAMZI) modulator structure [11, 12]. In these devices the inherent nonlinearity of the Mach-Zehnder Interferometer (MZI) plus the nonlinearity of the silicon phase modulators is mitigated by the opposite sign of nonlinearity created in the highly coupled ring phase modulators. While the RAMZI modulator devices improved upon the performance of Si MZMs, and further improvement is expected if the nonlinearity cancellation is optimized, neither of these results approaches the performance of commercial LiNbO$_3$ MZMs.

In this work, the linearity performance of silicon photonics based MZMs using heterogeneously integrated phase modulation sections fabricated from hybrid III-V multiple quantum-well (MQW)/Si waveguides was investigated. The heterogeneous Si integration platform adopts a CMOS compatible process, with advantages in the flexibility of integrating different material systems on silicon-on-insulator (SOI) [13]. The use of hybrid III-V/Si phase modulation sections was chosen due to the lower nonlinearity of the III-V MQWs, together...
with the potential for higher optical power level operation, compared to Si based devices. The
design, fabrication and testing of these HS-MZM devices, is described in the following
sections.

2. Device design and fabrication

Unlike Si, the heterogeneously integrated III-V materials are not centro-symmetric and have
a direct bandgap, therefore, in addition to the plasma and Kerr effects present in Si, they also
exhibit a strong Pockels effect and quantum confined Stark effect (QCSE) [14, 15]. This
provides extra degrees of freedom to achieve high linearity MZMs, and also helps to increase
the modulation efficiency (lower $V_p$) plus provides for devices with larger bandwidth and a
compact footprint.

The refractive index in the heterogeneous waveguide changes as a function of the applied
voltage as:

$$\Delta n(\lambda, f) = \sum_{m=1}^{4} \sum_{k=1}^{4} c_{mk}(\lambda) k^k$$  \hspace{1cm} (1)

where $m = 1:4$ refers to the mechanism of the index change, respectively plasma effect, band
filling, QCSE and Pockels effect [14] and order $k$ is truncated at 4 because it is the fourth
order term that is responsible for the third order distortion. The applied voltage consists of
bias $V_b$ and signal $V_s(t)$, hence the index change can be expanded near the bias point as:

$$\Delta n(\lambda, f) = \sum_{m=1}^{4} \sum_{k=1}^{4} \left[ c_{mk} + (k+1)c_{mk,k+1}V_b + \ldots \right] V_s^k(t)$$  \hspace{1cm} (2)

The response of the MZI modulator biased at quadrature and operating in push-pull mode
can be then written as,

$$P_s \approx \sin \left( \frac{2\pi \Delta n L}{\lambda} \right) = \frac{2\pi \Delta n L}{\lambda} \frac{1}{6} \left( \frac{2\pi \Delta n L}{\lambda} \right)^3$$  \hspace{1cm} (3)

and substitution yields the third order distortion as,

$$P_s^{(3)} = \sum_{m=1}^{4} \left[ \frac{2\pi L}{\lambda} (c_{m3} + 4c_{m4}V_b) \right]^3 V_s^3(t) \frac{1}{6} \left[ \frac{2\pi L}{\lambda} \sum_{m=1}^{4} (c_{m1} + 2c_{m2}V_b + 3c_{m3}V_b^2 + 4c_{m4}V_b^3) \right]^3 V_s^3(t)$$  \hspace{1cm} (4)

The coefficients $c_{33}$, $c_{34}$ (plasma) and $c_{23}$, $c_{24}$ (band filling) are negative, while the
coefficients $c_{33}$, $c_{34}$ (QCSE) are positive and also large in absolute value [14]. In other words,
the QCSE does not saturate with applied voltage, at least for reasonably small voltages. For
the Pockels effect only the linear term $c_{31}$ is non-zero. Since (with exception of the Pockels
effect) coefficients are wavelength dependent, especially those associated with the QCSE, at
one particular wavelength the positive third order distortion of the QCSE will compensate the
negative distortions of band filling, plasma and the MZI itself.

The HS-MZM structures and the CMOS compatible process flow used to fabricate the
devices are shown schematically in Fig. 1. The devices are an MZI with balanced arms which
include the hybrid III-V/Si phase modulators with tunable Directional Coupler (DC) or
Multimode Interference (MMI) devices as the 3-dB power splitter and cominers forming the
MZI. NiCr heaters were used to tune the DC devices for 50/50 splitting, and were also used
within the arms of the MZI to thermally tune its operating point.

Fabrication was carried out using an SOI substrate with a 500 nm Si device layer and a 1
μm buried oxide layer. An ASML PAS 5500/300 DUV stepper with 248 nm light source and
150 nm minimum feature size was used. This enables a higher optical mode confinement
factor in the MQWs from a narrower width waveguide. Careful calibration was performed to
tune the dimension of the passive components such as directional couplers. The Si waveguide
was etched with SF6/C4F8 plasma, providing a propagation loss of $1.6 \pm 0.3$ dB/cm for the 600
nm waveguide width (Fig. 1(a)). Diced III-V epi was then transferred to the patterned SOI substrate with a low temperature molecular bonding technique (Fig. 1(b)) [16]. A reactive ion etch tool was utilized to etch the III/V p-mesa and stop in the InAlGaAs active region, which was cleared afterwards with a selective wet etch to expose the bottom n-type contact layer. Pd/Ti/Pd/Au and Pd/Ge/Pd/Au stacks were deposited for p-type and n-type ohmic contacts, respectively (Fig. 1(c)). A 1 μm SiO₂ insulation layer was deposited, then NiCr heaters were deposited on top of that layer. Large gold metal pads were deposited after through-via etches in the thick insulation layer (Fig. 1(d)).

The bonded III-V materials stack includes an active region with 15 InAlGaAs QWs with a photoluminescence (PL) wavelength centered at 1360 nm; the details shown in Table 1. The QWs were slightly n-doped to improve the high speed performance [14]. Separate confinement heterostructure (SCH) layers were designed properly to improve the optical mode confinement in the QWs. The doping level in the p-InGaAs layer was raised by 50% compared with a previously used design [14], to lower the p-contact resistance, which normally dominates the series resistance of devices with a narrow p-mesa. In order to optimize the modulator efficiency, the III-V phase modulation sections were aligned parallel to the [011] crystalline orientation, so that the Pockels effect adds constructively to the other index changes. A high transverse electric (TE) optical mode confinement of 26.9% in the MQWs was achieved with a 600 nm wide Si WG and a 2.5 μm mesa, as shown in the cross sectional plot in Fig. 2(a). Figures 2(b) and 2(c) shows SEM images of the III-V mesa with a 50 μm long taper between this hybrid section and the Si waveguide. The taper tip width is smaller than 200 nm, providing low coupling loss between the Si waveguide and the hybrid section.

Table 1: The epitaxial III-V layers

<table>
<thead>
<tr>
<th>Layer composition</th>
<th>Doping level</th>
<th>Thickness (nm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>P-type contact In₀.₅₃Ga₀.₄₇As</td>
<td>Zn: 1.5 × 10¹⁷</td>
<td>100</td>
</tr>
<tr>
<td>Band smooth layer</td>
<td>Zn: 3 × 10¹⁷</td>
<td>50</td>
</tr>
<tr>
<td>P-type cladding InP</td>
<td>Zn: 1.5 × 10¹⁷ -&gt; 5 × 10¹⁷</td>
<td>1500</td>
</tr>
<tr>
<td>SCH InGaAsP 1.25Q</td>
<td>Si: 1 × 10¹⁰</td>
<td>100</td>
</tr>
<tr>
<td>15 × QW In₀.₇₅Al₀.₂₅Ga₀.₅₁₅As</td>
<td>Si: 1 × 10¹⁰</td>
<td>8</td>
</tr>
<tr>
<td>16 × Barrier In₀.₆₆Al₀.₃₄Ga₀.₃₅₂As</td>
<td>Si: 3 × 10¹⁹</td>
<td>8</td>
</tr>
<tr>
<td>SCH InGaAsP 1.25Q</td>
<td>Si: 3 × 10¹⁹</td>
<td>50</td>
</tr>
<tr>
<td>N-type contact InP</td>
<td>Si: 3 × 10¹⁹</td>
<td>110</td>
</tr>
<tr>
<td>Bonding layer with supper lattices</td>
<td>Si: 3 × 10¹⁹</td>
<td>40</td>
</tr>
</tbody>
</table>
3. Device characterization

The processed chips with polished facets were placed on a thermal heat sink which was temperature controlled at 25°C for testing. A single drive push-pull electrical configuration was utilized in the devices for ease of modulation [6], requiring only one RF drive signal, and to take advantage of the reduced capacitance of that approach. A tunable laser source with TE polarization and polarization maintaining (PM) fiber output was coupled into the Si waveguide using a PM lensed fiber. The output of the HS-MZM was coupled into a fiber using a fiber lens, and for SFDR measurements was amplified by an Er-Doped Fiber Amplifier (EDFA) and coupled into a photo detector (PD). Two HS-MZM devices are shown in Fig. 3: the left one uses an MMI coupler as the 3 dB power splitter in the MZI; the right one, which is fiber coupled in the image, uses a tunable directional coupler that can be tuned to a 3 dB split ratio according to the input wavelength. The input and output waveguides were angled to reduce optical reflections; however, the tapered design produces relatively high coupling loss (8 to 10 dB per facet) although with relatively easy alignment. Future devices will incorporate an inverse taper design [17] in order to provide much lower coupling loss, as required for a discrete modulator device.

DC measurements were taken under computer control to characterize single drive push-pull operation of the devices at multiple wavelengths. Figure 4 shows measurements of the output versus the differential voltage between the two phase modulation sections, for a device with a short (100 μm) phase modulation section length, and for different input optical wavelengths of 1550 nm, 1500 nm and 1460 nm. Much higher modulation efficiency (lower $V_\pi$) is seen at the shorter wavelength, closer to the band-edge off the phase modulator epi material. The device is biased near quadrature for all of the transfer characteristics shown in Fig. 4. The $V_\pi L$ of the device can be fit from this measured data, which is a minimum of 1.5 V·mm at 1460 nm, and larger at longer wavelength. The actual $V_\pi$ is higher in this particular device due to its short length. The larger $V_\pi$ compared with previous results on similar hybrid silicon MZI modulators [14], probably results from a reduction of mode confinement in the MQWs of a narrower III/V mesa, from undercut of the MQWs layers in the wet-etch step, or the voltage drop on a higher series resistance due to the narrower mesa.
The apparent linearity of the DC characteristics may change with wavelength, indicating a delicate interplay of the different phase modulation mechanisms described above, however, this is difficult to quantify from DC measurements alone. The third order nonlinearity, i.e. the third order term in the series expansion, of the sinusoidal-like response of the MZI is negative. Also negative are the third order terms in the series expansion of the plasma-like effect of free carriers and the state-blocking phase change effect. At the same time, the phase characteristics of the QCSE have a positive third order term, and also a strong wavelength dependence. The fourth phase modulation mechanism, the Pockels effect, is largely linear. As a result, at some wavelength and modulator bias the positive nonlinearity of the QCSE phase response could become sufficient to cancel the negative nonlinearities of the plasma and state blocking effects, and potentially also the nonlinearity of the MZI itself. By choosing the optimum bias point this high linearity wavelength can be shifted towards the desired value of 1550nm, however, complete optimization, and therefore linearization of the response, may require an MQW design with a longer PL wavelength than used in these devices.

Fig. 4. Transmission measurement of 100 μm HS-MZM at multiple wavelengths.

Fig. 5. Measured Frequency Response for 100 μm HS-MZM: unterminated, 0V, −3V; 50 Ohm −3V.
Frequency response measurements of the 100 μm device, demonstrating a bandwidth of over 20 GHz, are shown in Fig. 5. Terminating the device with a 50 Ohm load shows little improvement in device bandwidth (up to 20 GHz), while reducing the efficiency by 3 dB compared to no matching load. Biasing the device at 0 V shows an increase in low frequency response, likely due to the small swing to positive bias from the sinusoidal input.

To assess linearity, measurements of the SFDR of this same device were carried out at a wavelength of 1550 nm, and a modulation frequency of 10 GHz. An EDFA was used to overcome the loss of the two fiber couplings (16 to 20 dB), plus provide sufficient optical power at the PD to support high SFDR operation. The requirement to use an EDFA limited the wavelength range over which SFDR could be measured, limiting the possibility to further improve the performance by increasing the QCSE. As shown in Fig. 3, two RF tones were combined using a power combiner and RF pads and applied to the device. The separation between two tones was set to 10 MHz, so the two signals were at 9995 MHz and 10005 MHz for the 10 GHz center frequency. The generated third order intermodulation distortion signals were at 9985 MHz and 10015 MHz, respectively. The fundamental and third order intermodulation products were measured on a high performance electrical spectrum analyzer.

The SFDR was measured versus variations in the input optical power level to the MZM, versus modulator p-n junction dc bias voltage, and for different photodetector currents. A series of SFDR traces were taken, with the highest SFDR occurring for the highest optical input power level (+18 dBm laser output) and also highest photocurrent (15.6 mA), for a DC bias voltage of −2 V. The measured response in Fig. 6 shows the highest SFDR; in this case the plot has an SFDR of 52 dB for 1 GHz bandwidth, or an SFDR of 112 dB·Hz^{2/3}.

Figure 7 compares major results of recent works on the linearity of modulators fabricated on a Si, CMOS compatible platform [10, 12, 18–23]. This work surpasses the previous record for a linearized modulator on Si, obtained using the RAMZI modulator design [12], without the need of coupled microresonators and their wavelength dependent operation. It is significantly higher than any Si MZM result. This result demonstrates that heterogeneously integrated III-V/Si modulators, using wafer bonded III-V material for phase modulation sections, can produce performance similar to Lithium Niobate devices [9], eliminating the nonlinearity added by using Si phase modulators, and provides a promising solution for highly integrated RF photonics applications.
4. Conclusions

In this work we have demonstrated that high SFDR of up to 112 dB·Hz$^{2/3}$ at a 10 GHz modulation frequency can be achieved in a heterogeneously integrated III-V on Si MZM, a value similar to that achieved using commercial LiNbO$_3$ MZMs. The high SFDR is achieved through the use of low nonlinearity III-V MQW phase modulation sections, the device biased to minimize nonlinear effects, leaving only the nonlinearity of the MZI itself. Further optimization offers the opportunity to linearize the MZI response itself, by increasing the QCSE in the phase modulation sections. This result enables the development of high performance RF photonics systems on a CMOS compatible platform that includes heterogeneous integration of III-V materials.

Acknowledgment

The authors would like to thank Morton Photonics and DARPA for supporting this work under STTR program ‘Miniature Silicon WDM Modulators for Analog Fiber-Optics Links’, contract # W91CRB-10-C-0099. The authors would also like to thank the nano-fabrication facility at UC Santa Barbara, and Shangjian Zhang, Michael L. Davenport, Sudharsanan Srinivasan, and Geza Kurczveil for useful discussions.